

ABSTRACT

Terminating resistances are provided to at least some pins of an ASIC or other multi-pin
10 component mounted on a surface of a circuit board, by positioning a second circuit board on the
surface of the main circuit board substantially opposite, and preferably aligned with or
overlapping, the multi-pin component. The second circuit board accommodates a resistor such
as a printed resistor, surface mount resistor or buried resistor. Preferably, vias in the main circuit
board connect pins of the ASIC to terminating resistors. Preferably one or both of the ASIC and
15 the second circuit board are coupled to the main circuit board by a ball grid array.

M:\4103\67101\Patent Application.wpd